

THICK FILM LADDER NETWORKS

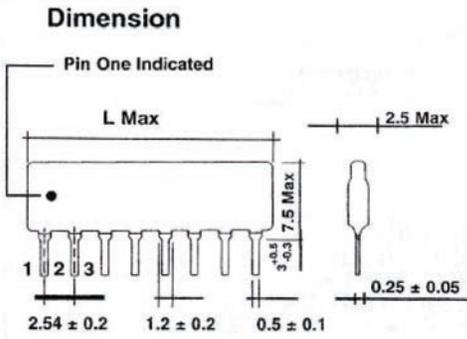


L2R

LEAD FREE

L2R

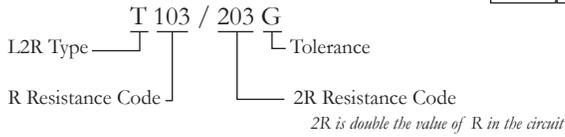
Single Inline Package - R/R2 Ladder



Bits	Number of pins	L max. (mm)
4	6	15.3
5	7	17.8
6	8	20.4
7	9	22.9
8	10	25.4
9	11	28.0
10	12	30.5

	4BITS	5BITS	6BITS	7BITS	8BITS	9BITS	10BITS
Standard R Resistance Value	5K Ohm - 10K Ohm - 25K Ohm - 50K Ohm - 100K Ohm						
Bit Errors	±1/2 LSB Max.						
FSA (max.)	3.12%	1.56%	0.78%	0.39%	0.2%	0.2%	0.2%
Output Impedance Characteristic	G=±2%, J=±5%						
Rated Power	30mW/element						
Working Voltage	100Volts						
Operating Temperature Range	-55°C ~ +125°C						
T.C. of Output Impedance	0±200ppm/°C, Tracking ±50ppm/°C						
T.C. of Bit Voltage	0±30ppm/°C						

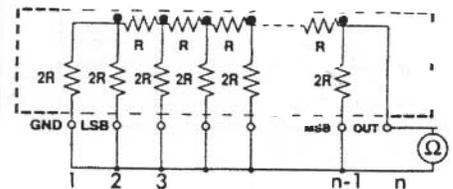
Typical Part Marking:



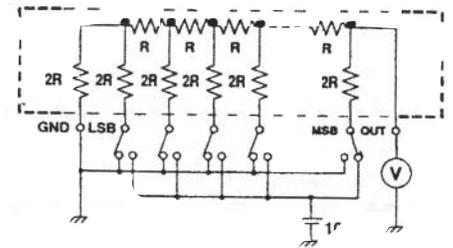
Example with 10KΩ 2% = Resistance Code = 103, Tolerance Code = G

PART NO.	CIRCUITS
L2R 4BIT 103G	
L2R 5BIT 103G	
L2R 6BIT 103G	
L2R 7BIT 103G	
L2R 8BIT 103G	
L2R 9BIT 103G	
L2R 10BIT 103G	

Output Impedance Testing Circuit

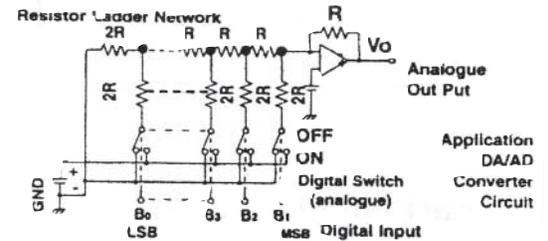


Linearity Testing Circuit



Application

R/R2 Ladder Networks for D/A and A/D converter with bi-polar or CMOS switches.



Part Numbering System

